

Analinear is an imaging solution company, providing Imaging Sensor IP's, Peripheral Analog IP's, Imaging SoC design and Specialized camera System products.

RTL Design Engineer (Intern)

Job description:

- Responsible for IP / sub-system level RTL coding
- Perform Behavioural & Timing Simulations
- Perform basic verification of IP designed on FPGA
- Integration & validation of the IP's

Required Candidate profile

- Btech or Mtech degree in Electronics.
- Proficient in Verilog RTL coding
- Knowledge on Multiple Interface protocols (SPI, UART, I2C...)
- Expertise in Digital Electronics

Stipend

25,000 per month

Contact